Received

SEP 0 5 2002

Received

SEP 0 5 2002

Technology Center 2100

Technology Center 2100

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

ubmissim of Missing 16 pils.

In re application of:

Confirmation No. 2552

Timothy J. VAN HOOK et al.

Art Unit: 2552 2/83

Appl. No.: 09/662,832

Examiner: Pan, D.

Filed: September 15, 2000

Atty. Docket: 1778.0100002

For:

Alignment and Ordering of Vector

Atty. Docket. 1778.0100002

Elements for Single Instruction

Multiple Data Processing

SEP 0 6 2002

Received

Multiple Data Processing

Technology Center 2100

the may fredoz

Letter Re-Submitting Non-U.S. Patent Documents

Commissioner for Patents Washington, D.C. 20231

HANDCARRY TO:
Mr. Daniel Pan, CP 2, 2A05
4th Floor Reception

Sir:

In accordance with a request from Examiner Daniel Pan, on August 28, 2002, Applicants hereby re-submit copies of the non-U.S. Patent documents previously filed with an Information Disclosure Statement on May 22, 2002.

Applicants also hereby re-submit a copy of the form PTO-1449 listing all of the documents filed on May 22, 2002. Per a telephone conversation and agreement with Examiner Pan on September 5, 2002, the cited U.S. Patent documents are not being re-submitted. The re-submitted documents only include copies of the documents identified on the PTO-1449 with document reference numbers AJ1-AR1, AN2-AR2, AN3-AR3, AN4-AR4, AN5-AR5, AN6-AR6, AN7-AR7 and AN8-AR8.

Applicants note that the copy of the document No. AO4 submitted herewith is missing pages A-122 and A-172.

Timothy J. VAN HOOK et al. Appl. No. 09/662,832

- 2 -

It is respectfully requested that the attached postcard be stamped with the date of resubmission of the above listed documents, and that it be returned to our courier. In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

-Michael B. Ray

Attorney for Applicants Registration No. 33,997

Date:

1100 New York Avenue, N.W. Washington, D.C. 20005-3934 (202) 371-2600

::ODMA\MHODMA\SKGF_DC1;53013;1 SKGF Rev. 1/28/98 dcw

DOLLARS 15-122 540 Box: RCE Alignment and Ordering of Vector Elements for Single Instruction Multiple Data Atty: MBR/BAM/mjg **Docket:** 1778.0100002 THE SUM OF 74C MCCCC When receipt stamp is placed hereon, the USPTO acknowledges receipt of the following documents: Examiner: Pan, D. Art Unit: 2183 Due Date: Please Date Stamp And Return To Our Courier 178.0100002 Request for Continued Examination (RCE) Transmittal (PTO/SB/30); Our Check No. 35238 for \$ 740.00 to cover RCE application filing. A list of the cited documents on Form PTO-1449 (8 pages); A copy of the (88) documents cited on Form PTO-1449; STERNE, KESSLER, GOLDSTEIN & FOX PLLC Preliminary Amendment Under 37 C.F.R. § 1.114 COMM. OF PATENTS & TRADEMARKS 1100 NEW YORK AVE. NW, SUITE 600 WASHINGTON, D.C. 20005 PTO Fee Transmittal Form (PTO/SB/17); September 15, 2000 Applicant: VAN HOOK et al. GOVERNMENT FEES ACCT. Information Disclosure Statement; **ATTORNEYS AT LAW** TO THE ORDER O Processing 09/662,832 Return postcard; and SKGF Cover Letter Filed: Application No.: For: SKGF_DC1:8663.1 PA∀

FIRST UNION BANK • WASHINGTON, D.C. 20005

Same			•			A 1	TTY. DOCKET NO. 778.0100002 (0055.2	20US)	APPLI 09/66	CATION NO. 2,832		
		FORM F	то-1449			APPLICANT						
Coli	FORMAT	ON DIS	CLOSURE STATE	MENT			an Hook et al.		<u> </u>		<u> </u>	
as 174	15	\	D.P. 09/	6 9 /0	72		ILING DATE eptember 15, 2000		GROUP 2183			
m M	ay 2		12			ATE	NT DOCUMENTS					
EXAMINER	1				_							
INITIAL		NUM	UMENT BER	DATE	=	NA	ME	10	CLASS	SUB- CLASS	FILING DATE	
	AA1	+	4,621	04/19	72	Boo	ck et al.	3	340	172.5		
	AB1	3,91	6,388	10/19	75	Shi	mp <i>et al</i> .	3	40	172.5		
	AC1	4,02	3,023	05/19	77	Воι	ırrez et al.	2	235	152		
	AD1	4,10	9,310 \	08/19	978	Eng	gland <i>et al.</i>	3	64	200		
	AE1	4,12	8,880	12/19	78	Cra	y, Jr.	3	64	200		
	AF1	4,13	0,880	12/19	978	Nut	ter	3	64	900		
	AG1	4,21	9,874	08/19	980	Gu	sev et al.	3	864	200		
	AH1	4,31	7,170	02/19	982	Wa	da <i>et al</i> .	3	864	200		
	Al1	4,39	6,982	Q8/19	983	Wa	da et al.		864	200		
		,		\rightarrow	FOREIGN	I PA	TENT DOCUMENTS					
EXAMINER INITIAL		DOC	CUMENT NUMI	BER	DATE		COUNTRY		CLASS	SUB- CLASS	TRANSLATION	
	AJ1	wo	97/07450 A1		02/1997		PCT	(306F	7/00	N/A	
	AK1	wo	97/08608 A1		03/1997		PCT	(306F	7/00	. N/A	
	AL1	0 68	1 236 A1		11/1995		EP	(306F	9/302	N/A	
	AM1	0 68	1 236 B1	·	11/2000	$\overline{\ \ }$	EP	(306F	9/302	N/A	
			OTHE	R (Inc	luding Aut	hok,	Title, Date, Pertinen	t Pages, etc.)				
				·			\					
	AN	1	An Introduction	on to T	<i>humb™</i> , Vei	rsion	2.0, Advanced RISC	Machines, 29	pages (N	March 1995).		
	AO	1	Bier, J., "DSF (September 1			mmu	nications Apps," Mica	roprocessor R	eport, Mid	croDesign Re	sources, pp. 11-15	
	АР	<u>1</u>	Case, B., "Su Vol. 5, No. 22	persca 2, pp. 1	alar Techniq and 6-11 ([ues: Dece	SuperSPARC vs. 88 mber 4, 1991).	110," <i>Micropro</i>	cessor R	eport, MicroD	Design Resources,	
	AQ	1	Diefendorff, k	<., "884 _	110 Second	Leve	l Cache," <i>Microproce</i>	essor Forum, N	1otorola l	nc., 20 pages	(November 1991).	
Á	AR	1	Diefendorff, k pages, Sourc				nization of the Motor	ola 88110: A S	Superscal	ar RISC Micr	oprocessor," 12	
EXAMINER								DA	TE CON	SIDERED		
							n conformance with N		aw line th	rough citatio	n if not in	
conformance and	not con	sidered	a. Include copy	of thi	s form with r	next	communication to Ap	plicant.				

ATTY. DOCKET NO. APPLICATION NO. 1778.0100002 (0055.20US) 09/662,832 FORM\PTO-1449 APPLICANT Van Hook et al. INFORMATION DISCLOSURE STATEMENT GROUP FILING DATE 2183 September 15, 2000 **U.S. PATENT DOCUMENTS EXAMINER** DOCUMENT DATE NAME CLASS SUB-FILING DATE INITIAL NUMBER CLASS 364 AA2 4,491,910 01/1985 Caudel et al. 200 200 364 AB2 Morrison 4,507,731 03/1985 364 AC2 4,520,439 05/1985 Liepa 200 364 AD2 04/1986 Boothroyd et al. 900 4,583,199 AE2 4,773,006 09/1988 Kinoshita et al. 364 200 AF2 03/1989 364 200 4,814,976 Hansen et al. 364 ****09/1992 Hunt 736 AG2 5,150,290 05/1995 AH2 5,418,915 Matuda et al. 395 325 04(1996 395 800 AI2 5,513,366 Agarwal et al. **FOREIGN PATENT DOCUMENTS EXAMINER** INITIAL DOCUMENT NUMBER DATE COUNTRY **CLASS** SUB-**TRANSLATION CLASS** AJ2 Yes No AK2 Yes No AL2 Yes No AM2 Yes No OTHER (Including Author, Title, Date, Pertinent Pages, etc.) Fiegel, C.P., "TI Introduces Four-Pipcessor DSP Chip," Microprocessor Report, MicroDesign Resources, pp. 22-AN 2 25 (March 28, 1994). Gwennap, L., "Digital, MIPS Add Multimedia Extensions," Microprocessor Report, MicroDesign Resources, Vol. AO 2 10, No. 15, pp. 1-5 (November 18, 1996). Gwennap, L., "New Multimedia Chips to Enter the Fray," Microprocessor Report, MicroDesign Resources, Vol. ΑP 2 10, No. 13, 1 page (October 1996). Hansen, C., "Architecture of a Broadband Mediaprocessor," COMPCON 96, IEE, pp. 1-8 (February 25-29, AQ 2 1996). DSP56002 24-Bit Digital Signal Processor User's Manual, Motorola, Inc., 386 pages (1995). (Bates Numbers AR 2 L07277-L07662). **EXAMINER** DATE CONSIDERED EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

::ODMA\MHODMA\SKGF_DC1;11089;2

FORM PTO-1449
INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO. 1778.0100002 (0055.20US) APPLICATION NO. 09/662,832

APPLICANT

			\				n Hook et al.				
<u>'</u>	INFORMAT	ION DIS	CLOSURE STATE	<u>MENT</u>		FIL	ING DATE		GROUP 2183		
	7		========	\	U.S. F	PATEN	T DOCUMENTS				
EXAMINER INITIAL			CUMENT MBER	DATE	<u>=</u>	NAM	E		CLASS	SUB- CLASS	FILING DATE
	AA3	5,58	1,773	12/1	996	Glov	er		395	800	
	AB3	5,59	0,345	12/19	96	Bark	er et al.		395	800	
	AC3	5,66	6,298	09/19	99,7	Pele	g et al.		364	715.08	
	AD3	5,66	9,010	09/19	99₹	Dulu	k, Jr.		395	800.22	
	AE3	5,72	1,892	02/19	998 \	Pele	g et al.		395	562	
	AF3	5,73	4,874	03/19	998	Van	Hook <i>et al.</i>	_	395	513	
	AG3	5,74	0,340	04/19	998 \	Purc	ell et al.		395	118	
	AH3	5,75	2,071	05/19	998	Tubb	s et al.		395	800.34	
	Al3	5,75	8,176	05/19	998	Agar	wal et al.		395	800	
					FOREIG	NPAT	ENT DOCUMENTS				
EXAMINER INITIAL		DOC	CUMENT NUMI	BER	DATE	1	COUNTRY		CLASS	SUB- CLASS	TRANSLATION
	AJ3					1					Yes No
	AK3										Yes No
	AL3										Yes
	AM3				· · · · · · · · · · · · · · · · · · ·						Yes
			OTUE	ED /In	luding Au	ther T	itle, Date, Pertinent F	2000 0	<u> </u>		No No
	AN	3	Peleg, A. and (August 1996	l Weis	er, U., "MM	X Tech	nology Extension to th	ne Intel A	rchitecture,"	IEEE Micro,	IEEE, pp. 42-50
	AO	3	QML-DSP/Mi Inc., 7 pages	CM an (Janu	d Die Couri ary 1996).	ier, at h	ttp://www.ti.com/sd/do	ocs/militar	y/liter/ecour	dsp.htp, Tex	cas Instruments,
	AP	3	Turley, J. and MicroDesign	l Hakk Resou	arainen, H. Irces, pp. 14	, "Tl's N 4-17 (F	New 'C6x DSP Scream ebruary 17, 1997).	ns at 1,60	0 MIPS," <i>M</i>	icroprocesso	r Report,
	AQ	3	DSP56002 24 (Bates Numb				ssor Semiconductor T	echnical	Data, Motor	ola, Inc., 110	pages (1996).
-	AR	3	A236 Parallel pages (1994)	<i>Digita</i> . (Bat	al Signal Pro es Numbers	ocessor s L1118	r Chip Programmer's F 34-L11378).	Reference	e Manual, Q	xford Micro D	Devices, Inc., 195
EXAMINER		1	<u> </u>						DATE CON	SIDERED	

FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO. 1778.0100002 (0055.20US) APPLICATION NO. 09/662,832

APPLICANT

	M. O.M. IA. I	ON DISCLOSURE STATE	111214						
						ING DATE otember 15, 2000	GR0 218	DUP 33	
		\		U.S. P		T DOCUMENTS			
EXAMINER INITIAL		DOCUMENT NUMBER	DAT	E	NAM	E	CLASS	SUB- CLASS	FILING DATE
	AA4	5,761,523	06/19	998	Wilki	nson <i>et al.</i>	395	800.2	
	AB4	5,778,241	07/19	998	Bind	oss et al.	395	800.2	
	AC4	5,784,602	07/19	998	Glas	s et al.	395	561	
	AD4	5,793,661	08/19	998	Dulo	ng <i>et al.</i>	364	750.5	
	AE4	5,812,147	09/19	998	Van	Hook <i>et al</i> .	345	511	
	AF4	5,812,723	09/19	998	Ohts	u <i>et al</i> .	385	128	
	AG4	5,815,723	09/19	998	Wilki	nson <i>et al</i> .	395	800.2	
	AH4	5,822,606	10/19	98	Mort	on	395	800.16	
	Al4	5,838,984	11/19	998	Nguy	en et al.	395	800.05	
				FOREIGI		ENT DOCUMENTS	•		
EXAMINER INITIAL		DOCUMENT NUM	BER	DATE		COUNTRY	CLASS	SUB- CLASS	TRANSLATION
	AJ4				<u> </u>				Yes No
	AK4								Yes No
	AL4								Yes ` No
	AM4								Yes No
		ОТН	ER (In	cluding Aut	hor, T	itle, Date, Pertinent Pa	ges, etc.)		
	AN	Lee, R., "Acc (Bates Numb	elerati ers L0	ng Multimed 8566-L0857	lia with '6).	Enhanced Microproces	sors," <i>IEEE Mic</i> .	ro, IEEE, pp. 22	2-32 (April 1995).
	AO	4 DSP56000 2 L08722-L093	4-Bit © 59).	Digital Signal	Proce	ssor Family Manual, Mo	torola, Inc., 638	pages (1995).	(Bates Numbers
	АР	4 Microprocess	or De	vices Reliab	ility Mo	onitor Report: Third Qua	rter 1997, Motor	ola, 8 pages (19	997).
	AQ	4 Reliability Mo	nitor E	Environment	al Stre	ss Data 3Q97, Motorola	, 4 pages (1997)		
	AR	4 Reliability and	d Qual	ity Report 30	Q97, M	lotorola, 2 pages (1997)		\	
EXAMINER	1ļ			· · ·			DATE CO	NSIDERED	

ATTY. DOCKET NO. APPLICATION NO. 1778.0100002 (0055.20US) 09/662,832 FORM RTO-1449 APPLICANT Van Hook et al. INFORMATION DISCLOSURE STATEMENT GROUP FILING DATE September 15, 2000 2183 **U.S. PATENT DOCUMENTS EXAMINER** DATE NAME **CLASS** SUB-FILING DATE **DOCUMENT** INITIAL **CLASS** NUMBER 800.22 395 AA5 5,864,703 01/1999 van Hook et al. Park et al. 395 800.23 AB5 5,881,307 03/1999 Fischer et al. 364 754.03 AC5 5,936,872 08/1999 371 09/1999 Spracklen 53 AD5 5,960,012 712 AE5 5,996,056 11/1999 Volkonsky 1 12/1999 712 22 Dinkjian AF5 6,006,316 05/2000 712 7 Nguyen AG5 6,058,465 Morton 07/2000 712 22 AH5 6,088,783 AI5 FOREIGN PATENT DOCUMENTS **EXAMINER** INITIAL DOCUMENT NUMBER DATÊ COUNTRY **CLASS** SUB-TRANSLATION **CLASS** AJ5 Yes No Yes AK5 No AL₅ Yes No Yes AM5 No OTHER (Including Author, Title), Date, Pertinent Pages, etc.) ΑN <u>5</u> Microprocessor Devices Reliability Monitor Report: Fourth Quarter 1997, Motorola, 10 pages (1997). AO Reliability and Quality Report 4Q97, Motorola, 2 pages (1997). <u>5</u> "Hardware Implications of xmem as a st followed by a Id," Motorola Semiconductor Engineering Bulletin, ΑP <u>5</u> Motorola, 5 pages (September 1992). 601 First Silicon, at http://www.mot.com/SPS/PowerPC/library/press_releases/601_First_Silicon.html, Motorola, 2 AQ <u>5</u> Pages (October 1, 1992). DSP56002/DSP56L002 24-Bit Digital Signal Processor Motorola Semiconductor Product Information, Motorola, AR 5 Inc., 3 pages (1994). (Bates Numbers L07913-L07915). **EXAMINER** DATE CONSIDERED **EXAMINER**: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

							78.0100002 (0055.20L	JS)		2,832	
•	,	FORM	PTO-1449			AP	PLICANT				
	INFORMAT I	ON DI	SCLOSURE STATE	MENT			n Hook et al.		T		
							LING DATE ptember 15, 2000		GROUP 2183		
			\		U.S. P.	ATEN	T DOCUMENTS				
EXAMINER INITIAL		DO	CUMENT MBER	DATI	Ē	NAM	1E		CLASS	SUB- CLASS	FILING DATE
	AA6		1		•						
	AB6		· E								
	AC6										
	AD6		***								
	AE6		W.								
	AF6			à							
	AG6			V							
	AH6			-							
	Al6			¥	· A						
					FOREIGN	I PAT	ENT DOCUMENTS				
EXAMINER INITIAL		DO	CUMENT NUM	BER	DATE		COUNTRY	,	CLASS	SUB- CLASS	TRANSLATION
	AJ6				Y W						Ye N
	AK6				P. A. S.						Ye N
	AL6					Contract in					Ye N
	AM6					; is a second					Ye N
			ОТНЕ	R (In	cluding Aut	hor, T	itle, Date, Pertinent P	ages, etc	;.)		
	AN	<u>6</u>	MC88410UM sps.com/lit/ht	/AD: M ml/mc	//C88410 Sec 88410umad.	conda .html,	nry Cache Controller Us Motorola, Inc., 1 page	ser's Mana (January	<i>ual, at</i> http:/ 24, 1997).	//www.mot-	
	AO	<u>6</u>	AN1214: MCc sps.com/lit/ht	88110 ml/an	<i>64-bit Exteri</i> 1214.html, M	nal Bu lotorol	is Interface to 16-bit El a, Inc., 1 page (Januar	PROM, at ry 24, 199	http://www 7).	.mot-	
	AP	<u>6</u>	TMS320C80	Digitai	l Signal Proc	essor	, Texas Instruments Co	orp., p. 41	(July 1994).	
	AQ	<u>6</u>	EB162: Progr (January 24,	rammii 1997).	ng Tips (MC	88110), <i>at</i> http://www.mot-sp	es.com/lit/	ntml/eb162	.html, Motoro	la, Inc., 1 page
-	AR	<u>6</u>	MC88110/410 sps.com/lit/ht	0DH/A ml/mc	<i>D: MC88110</i> 88110410dh	<i>)/MC8</i> ad.htr	8410 Designer's Hand nl, Motorola, Inc., 1 pa	<i>book</i> , <i>at</i> h ge (Janua	ttp://www.n ry 24, 1997	not- ').	
EXAMINER		<u> </u>	<u> </u>						ATE CONS	SIDERED	

Page 7 of 8

ATTY. DOCKET NO. 1778.0100002 (0055.20US)

APPLICATION NO. 09/662,832

FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

APPLICANT Van Hook et al.

GROUP FILING DATE

						ING DATE tember 15, 2000		GROUP 2183	ı	
				U.S. F		DOCUMENTS				
EXAMINER INITIAL		DOCUMENT NUMBER	DAT		NAME	· -		CLASS	SUB- CLASS	FILING DATE
	AA7									
	AB7									
	AC7									
	AD7									
	AE7				<u></u>			ļ		
	AF7									
	AG7									
	AH7							<u> </u>		
	AI7					<u> </u>				<u> </u>
				FOREIG	N PATE	NT DOCUMENTS		1		
EXAMINER INITIAL		DOCUMENT NU	MBER	DATE		COUNTRY		CLASS	SUB- CLASS	TRANSLATION
	AJ7									Yes No
	AK7									Yes No
	AL7									Yes No
	AM7									Yes No
		от	HER (In	cluding Au	thor, Ti	tle, Date, Pertinent Pa	iges, e	tc.)		•
	AN	7 MC88110L sps.com/lit	/MAD: M /html/mo	<i>1</i> C88110 Se c88110umad	cond Ge d.html, N	eneration RISC Microp Motorola, Inc., 1 page (rocesso January	or User's Ma v 24, 1997).	nual, at http:	//www.mot-
	AO	7 Weiss, R., page, (Nov			ARM R	ISC with DSP, at http://	/ww.est	td.com/Edito	rial/1996/11/	Briefs/arm.html, 1
	AP			C2xx/C5x As nbers L0791		Language Tools User': 98).	s Guide	e, Texas Inst	ruments, 483	B pages (March
	AQ	7 TMS320C5 (Bates Nur	ox General	<i>ral-Purpose</i> 08399-L0856	Applica 65).	tions User's Guide, Tex	xas Inst	truments, 16	7 pages (Jul	y 1997).
-	AR	7 El-Sharkaw Prentice Ha	vy, Moha all PTR,	amed, Ph.D. pp. 43-124 (., <i>Digital</i> (1996).	Signal Processing App (Bates Numbers L065	olicatior 19-L06	ns with Moto 601).	rola's DSP56	6002 Processor,
EXAMINER		<u> </u>						DATE CON	SIDERED	

ATTY. DOCKET NO. APPLICATION NO. 09/662,832 1778.0100002 (0055.20US) FORM PTO-1449 **APPLICANT** Van Hook et al. INFORMATION DISCLOSURE STATEMENT **GROUP** FILING DATE 2183 September 15, 2000 **U.S. PATENT DOCUMENTS EXAMINER** DOCUMENT NUMBER INITIAL DATE NAME CLASS SUB-FILING DATE **CLASS** AA8 AB8 AC8 AD8 AE8 AF8 AG8 AH8 AI8 FOREIGN PATENT DOCUMENTS **EXAMINER** INITIAL DOCUMENT NUMBER DATE COUNTRY CLASS SUB-TRANSLATION **CLASS** AJ8 Yes No AK8 Yes No AL8 Yes No 8MA Yes No OTHER (Including Author, Title, Date, Pertinent Pages, etc.) AN 8 Killian, E., "MIPS Extensions fol Digital Media," Microprocessor Forum, pp. 1-5 (October 22-23, 1996). "Interrupt Latency in the MC88110," Motorola Semiconductor Engineering Bulletin, Motorola, pp. 1, 2 and 4-9 ΑO <u>8</u> (1992).AP "Running the MC88110 in Lockstep," *Motorol*a *Semiconductor Engineering Bulletin*, Motorola, 2 pages (1992). 8 Turley, J., "Siemens TriCore Revives CISC Techniques: New 32-Bit Design Emphasizes DSP Capability and AQ 8 Microcontroller Functions," Microprocessor Report, Microdesign Resources, pp. 13-16 (November 17, 1997). i860™ Microprocessor Family Programmer's Reference Manual, Intel Corporation, 79 pages (1992). (Bates AR 8 Numbers L09361-L09439). **EXAMINER** DATE CONSIDERED EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Drawline through citation if not in

conformance and not considered. Include copy of this form with next communication to Applicant.

Page 1 of 8 ATTY. DOCKET NO. APPLICATION NO. 1778.0100002 (0055.20US) 09/662,832 FORM RTO-1449 APPLICANT Van Hook et al. INFORMATION DISCLOSURE STATEMENT FILING DATE GROUP September 15, 2000 2183 **U.S. PATENT DOCUMENTS EXAMINER** INITIAL DOCUMENT DATE NAME **CLASS** SUB-FILING DATE NUMBER CLASS AA1 3,654,621 04/1972 Bock et al. 340 172.5 AB1 3,916,388 10/1975 Shimp et al. 340 172.5 AC1 4,023,023 05/1977 Bourrez et al. 235 152 08/1978 AD1 4,109,310 England et al. 364 200 12 1978 AE1 4,128,880 Cray, Jr. 364 200 AF1 12/1978 4,130,880 Nutter 364 900 08/1980 AG1 4,219,874 Gusev et al. 364 200 AH1 02/198 4,317,170 Wada et al. 364 200 AI1 4,396,982 08/1983 Wada et al. 364 200 FÖREIGN PATENT DOCUMENTS **EXAMINER** INITIAL DOCUMENT NUMBER DATE COUNTRY **CLASS** SUB-TRANSLATION CLASS AJ1 WO 97/07450 A1 02/1997 PCT G06F 7/00 N/A AK1 WO 97/08608 A1 **PCT** 03/1997 G06F 7/00 N/A AL₁ 0 681 236 A1 11/1995 FP G06F 9/302 N/A 0 681 236 B1 AM₁ 11/2000 EΡ G06F 9/302 N/A OTHER (Including Author, Title, Date, Pertinent Pages, etc.) ΑN 1 An Introduction to Thumb™, Version 2.0, Adyanced RISC Machines, 29 pages (March 1995). Bier, J., "DSP16xxx Targets Communications Apps," Microprocessor Report, MicroDesign Resources, pp. 11-15 AO 1 (September 15, 1997). Case, B., "Superscalar Techniques: SuperSPARC vs. 38110," Microprocessor Report, MicroDesign Resources, AP 1 Vol. 5, No. 22, pp. 1 and 6-11 (December 4, 1991). Diefendorff, K., "88410 Second Level Cache," Microprocesson, Forum, Motorola Inc., 20 pages (November AQ 1 1991). Diefendorff, K. and Allend, M., "Organization of the Motorola 88110: A Superscalar RISC Microprocessor," 12 AR 1 pages, Source and date unknown. **EXAMINER** DATE CONSIDERED **EXAMINER**: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

FORM PTQ-1449

ATTY. DOCKET NO. 1778.0100002 (0055.20US)

APPLICATION NO. 09/662,832

APPLICANT

INFORMATION DISCLOSURE STATEMENT

Van Hook et al.

FILING DATE September 15, 2000 GROUP 2183

							prember 13, 200		12183		
		<u> </u>		·	U.S. F	PATEN	T DOCUMENTS				
EXAMINER INITIAL			CUMENT MBER	DAT	E	NAN	1E		CLASS	SUB- CLASS	FILING DATE
	AA2	4,4	91,910	Q1/1	985	Cau	del <i>et al.</i>		364	200	
	AB2	4,5	07,731	03/1	985	Mor	ison		364	200	
	AC2	4,5	20,439	05/1	985	Liep	a		364	200	
	AD2	4,5	83,199	04/1	986	Boo	hroyd <i>et al</i> .		364	900	
	AE2	4,7	73,006	09/1	988	Kino	shita <i>et al.</i>		364	200	
	AF2	4,8	14,976	03/1	989\	Han	sen <i>et al.</i>		364	200	
	AG2	5,1	50,290	09/1	992	Hun			364	736	
	AH2	5,4	18,915	05/1	995	Matu	ida et al.		395	325	
	Al2	5,5	13,366	04/1	996	Agai	wal <i>et al</i> .		395	800	
					FOREIĞI	N PAT	ENT DOCUMENTS				
EXAMINER INITIAL		DO	CUMENT NUME	BER	DATE		COUNTRY		CLASS	SUB- CLASS	TRANSLATION
	AJ2		· · · · · · · · · · · · · · · · · · ·								Yes
	AK2										Yes No
	AL2										Yes No
	AM2										Yes No
			OTHE	R (Inc	cluding Aut	hor, T	tle, Date, Pertinent	Pages, e	etc.)		
	AO	<u>2</u>	22-25 (March	28, 19	994). al, MIPS Ado	d Multi	media Extensions " A				
	АР	2	Gwennap, L., 10, No. 13, 1 p	"New page (Multimedia October 199	Chips 96).	to Enter the Fray," <i>Mi</i>	icroproce	essor Report,	MicroDesign	Resources, Vol.
	AQ	<u>2</u>	Hansen, C., "A 1996).	Archite	ecture of a B	iroadba	and Mediaprocessor,	" COMPO	ON 96, IEE,	pp. 1-8 (Febr	uary 25-29,
	AR	2	DSP56002 24 L07277-L0766	-Bit Di 2).	igital Signal	Proces	ssor User's Manual, N	Motorola,	Inc., 386 pag	es (1995). (E	Bates Numbers
EXAMINER			<u></u>						DATE CONS	IDERED	

INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO. 1778.0100002 (0055.20US) APPLICATION NO. 09/662,832 APPLICANT Van Hook et al. FILING DATE

INE	PORMATIC	N DISCLOSURE	STATEME	ENT		in Hook et al.						
						LING DATE eptember 15, 2000	 	GROU 2183				
				U.S. P	ATEN	T DOCUMENTS						
EXAMINER INITIAL		DOCUMENT NUMBER	DAT	E	NAN	1E	CL	ASS	SUB- CLASS	FILING DATE		
	AA3	5,581,773	12/1	996	Glov	rer	39	 5	800			
	AB3	5,590,345	12/1	996	Bark	er et al.	39	5	800			
	AC3	5,666,298	09/1	997	Pele	g et al.	364	4	715.08			
	AD3	5,669,010	09/1	997	Dulu	k, Jr.	39	5	800.22			
	AE3	5,721,892	02/1	998	Pele	g et al	39	5	562			
	AF3	5,734,874	03/1		Van	Hook et al.	39	5	513			
	AG3	5,740,340	04/1		· 	ell et al.	39		118			
	AH3	5,752,071	05/1		 	os et al.	39		800.34	<u> </u>		
	AI3	5,758,176	05/1		<u> </u>	wal et al.	39	5	800	<u> </u>		
	<u> </u>	<u> </u>	-	FOREIGN	N PAT	ENT DOCUMENTS	 		· · · · · · · · · · · · · · · · · · ·	· r · · · · · · · · · · · · · · · · · ·		
EXAMINER INITIAL		DOCUMENT N	JMBER	DATE		COUNTRY	CL	ASS	SUB- CLASS	TRANSLATION		
	AJ3									Yes No		
	AK3									Yes No		
	AL3									Yes No		
	AM3									Yes No		
		Oi	HER (In	cluding Aut	outhor, Title, Date, Pertinent Pages, etc.)							
	AN	Peleg, A. (August 19	and Weis 996).	ser, U., "MMX	K Tech	nology Extension to the Inte	el Archite	cture,"	IEEE Micro,	IEEE, pp. 42-50		
	AO	3 QML-DSP Inc., 7 pag	<i>/MCM ar</i> les (Janu	nd Die Courie ary 1996).	<i>er, at</i> h	ttp://www.ti.com/sc/docs/m	ilitary/lite	/ecour/	dsp.htp, Tex	as Instruments,		
	АР	3 Turley, J. a	and Hakl gn Resou	carainen, H., urces, pp. 14	"Tl's N -17 (F	lew 'C6x DSP Screams at a ebruary 17, 1997).	1,600 MIF	PS," Mid	croprocessor	Report,		
	AQ	3 <i>DSP</i> 56002 (Bates Nu	2 24-Bit D	Digital Signal 17803-L07912	<i>Proce</i> 2).	ssor Semiconductor Techni	ical Data,	Motoro	ola, Inc., 110	pages (1996).		
•	AR	3 A236 Para pages (199	llel Digita 94). (Bat	al Signal Prod tes Numbers	cessoi L1118	r Chip Programmer's Refere 14-L11378).	ence Man	ual, Ox	ford Micro De	evices, Inc., 195		
EXAMINER				<u>.</u>			DATE	CONS	IDERED			

INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO. APPLICATION NO. 1778.0100002 (0055.20US) 09/662,832

APPLICANT

Van Hook et al.

FILING DATE September 15, 2000 GROUP 2183

						S∈	eptember 15, 2000		2183		
					U.S. P	ATEN	T DOCUMENTS				
EXAMINER INITIAL			CUMENT MBER	DAT	E	NAN	1E		CLASS	SUB- CLASS	FILING DATE
	AA4	5,70	61,523	06/1	998	Wilk	inson <i>et al</i> .		395	800.2	
	AB4	5,7	78,241	07/1	998	Bind	loss <i>et al</i> .		395	800.2	
	AC4	5,78	84,602	07/1	998	Glas	s et al.		395	561	
	AD4	5,79	93,661	08/1	998	Dulo	ng et al.		364	750.5	
	AE4	5,8	12,147	09/1	998	Van	Hook <i>et al.</i>		345	511	
	AF4	5,8	12,723	09/1	998	Ohts	su et al.		385	128	
	AG4	5,8	15,723	09/1	998	Wilk	inson <i>et al</i> .	395		800.2	
	AH4	5,82	22,606	10/1	998	Mort	on		395	800.16	
	Al4	5,83	38,984	11/1	998	Ngu	yen <i>et al.</i>		395	800.05	
					FOREIG	N PAT	ENT DOCUMENTS				
EXAMINER INITIAL		DO	CUMENT NUMI	BER	DATE		COUNTRY		CLASS	SUB- CLASS	TRANSLATION
	AJ4										Yes No
	AK4									•	Yes No
	AL4										Yes No
	AM4										Yes
			OTHE	D /In	oludina Aut	bor T	itle, Date, Pertinent P		<u> </u>		No
	AN	4	(Bates Number	ers L0	·8566-L0857	6).	Enhanced Microproce				
	АР	4			vices Reliabi	lity Mo	nitor Report: Third Qua	arter 199	97, Motorola,	8 pages (19	97).
	AQ	<u>4</u>	Reliability Moi	nitor E	Environmenta	al Stres	ss Data 3Q97, Motorola	a, 4 page	es (1997).		
-	AR	<u>4</u>	Reliability and	l Qual	ity Report 30	2 <i>97</i> , M	otorola, 2 pages (1997).		, 2.0	
EXAMINER	. I	<u> </u>	·				W	1	DATE CONS	IDERED	
											

Page 5 of 8

FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO. 1778.0100002 (0055.20US) APPLICATION NO. 09/662,832

APPLICANT Van Hook et al. GROUP

							LING DATE ptember 15, 2000		GROU 2183		
					U.S. F	PATEN	T DOCUMENTS				
EXAMINER INITIAL			CUMENT MBER	DAT	E	NAM	IE	-	CLASS	SUB- CLASS	FILING DATE
	AA5	5,86	64,703	01/1	999	van	Hook <i>et al</i> .		395	800.22	
	AB5		31,307	03/1	999		et al.		395	800.23	
	AC5		36,872	08/1	999		ner <i>et al</i> .		364	754.03	
	AD5	5,96	60,012	09/1	999	Spra	cklen		371	53	
	AE5		96,056	11/19			onsky		712	1	
	AF5		06,316	12/19		Dink			712	22	<u> </u>
	AG5		58,465	05/20	000	Ngu			712	7	
	AH5		88,783	07/20		Mort			712	22	
	AI5					1				1	
					FOREIG	N PAT	ENT DOCUMENTS				
EXAMINER INITIAL		DOG	CUMENT NUM	BER	DATE		COUNTRY		CLASS	SUB- CLASS	TRANSLATION
	AJ5										Yes No
	AK5	<u></u>		-							Yés No
	AL5										Yes No
	AM5										Yes No
	—-d		OTHE	R (Inc	luding Aut	hor, T	tle, Date, Pertinent Pa	ages.	etc.)	<u></u> J	1 10
	AN	<u>5</u>	Microprocess	or Dev	vices Reliab	ility Mc	nitor Report: Fourth Qu	uarter	1997, Motorol	a, 10 pages ((1997).
	AO	<u>5</u>	Reliability and	d Qual	ity Report 4	Q97, N	lotorola, 2 pages (1997).			
	АР	<u>5</u>	"Hardware Im Motorola, 5 pa	plicati ages (ons of <i>xmer</i> September	n as a 1992).	st followed by a ld," Mo	torola	Semiconducto	or Engineerin	g Bulletin,
	AQ	5	601 First Silic 2 Pages (Octo	on, at ober 1	http:/www.n , 1992).	not.con	n/SPS/PowerPC/library	/press	_releases/60 ²	_First_Silico	n.html, Motorola,
-	AR	<u>5</u>	DSP56002/DS Inc., 3 pages	SP56L (1994)	002 24-Bit L . (Bates Nu	Digital :	Signal Processor Motor L07913-L07915).	rola Se	miconductor I	Product Infort	mation, Motorola,
EXAMINER			<u> </u>		 				DATE CONS	IDERED	

Page 6 of 8

FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO. 1778.0100002 (0055.20US)	APPLICATION NO. 09/662,832
APPLICANT Van Hook et al.	
FILING DATE September 15, 2000	GROUP 2183

L							eptember 13, 2000		2103		
					U.S. P	ATEN	IT DOCUMENTS				
EXAMINER INITIAL			CUMENT MBER	DAT	E	NAM	ΛE		CLASS	SUB- CLASS	FILING DATE
	AA6	1		1		<u> </u>			1	 	1
	AB6								<u> </u>		
	AC6										
-	AD6	1		<u></u>							
	AE6	↓_				<u> </u>					
·	AF6	$+\!\!-\!\!\!-$		 		—					
	AG6	┼		—		—			 		
	AH6	+		+-		—				 	
	Al6			<u> </u>							
EXAMINER		т			TOREIGN	1 PAT	ENT DOCUMENTS			Т	
INITIAL		DOC	CUMENT NUM	BER	DATE		COUNTRY	· <u> </u>	CLASS	SUB- CLASS	TRANSLATION
	AJ6										Yes No
	AK6										Yes No
	AL6	<u> </u>									Yes No
	AM6										Yes No
			ОТНІ	ER (In	cluding Auti	hor, T	itle, Date, Pertinent Pa	ages, ef	tc.)		·
	AN	<u>6</u>	MC88410UM sps.com/lit/ht	VAD: M lml/mc	<i>1</i> C88410 Sec 88410umad.	condai .html, l	ry Cache Controller Use Motorola, Inc., 1 page (<i>er's Mar</i> January	nual, at http:// / 24, 1997).	/www.mot-	
	AO	<u>6</u>	AN1214: MCi sps.com/lit/ht	88110 lml/an1	<i>64-bit Exteri</i> I214.html, M	nal Bu lotorol:	es Interface to 16-bit EP a, Inc., 1 page (January	^P ROM, a y 24, 19!	ı <i>t</i> http://www.i 97).	mot-	
	АР	6	TMS320C80	Digital	Signal Proc	essor,	, Texas Instruments Cor	rp., p. 4	1 (July 1994)	·.	
	AQ	<u>6</u>	EB162: Progr (January 24,	<i>rammir</i> 1997).	ng Tips (MCE	38110)), at http://www.mot-sps	s.com/lit	/html/eb162.i	ntml, Motorola	a, Inc., 1 page
•	AR	6	MC88110/41(sps.com/lit/ht	0DH/A :ml/mc/	<i>D: MC88110</i> 88110410dh	<i>VMC8(</i> ad.htn	8410 Designer's Handb nl, Motorola, Inc., 1 pag	ook, at∃ je (Janu	http://www.m ary 24, 1997	ot-).	
EXAMINER									DATE CONS	IDERED	

ATTY. DOCKET NO. 1778.0100002 (0055.20US)

APPLICATION NO. 09/662,832

APPLICANT

INFORMATION DISCLOSURE STATEMENT

Van Hook et al.

FILING DATE September 15, 2000

GROUP 2183

					1 26	ptember 15, 2000		[2183		
·		· ·	····	U.S. F	PATEN	T DOCUMENTS				
EXAMINER INITIAL		DOCUMENT NUMBER	DAT	ΓE	NAM	E		CLASS	SUB- CLASS	FILING DATE
	AA7									
	AB7									
·	AC7									
	AD7									
**	AE7									
	AF7									
	AG7									
	AH7				-					
	AI7							.l. <u>.</u>		
				FOREIGI	N PATE	NT DOCUMENTS			<u></u>	···
EXAMINER INITIAL		DOCUMENT	NUMBER	DATE		COUNTRY		CLASS	SUB- CLASS	TRANSLATION
	AJ7									Ye No
	AK7									Yes No
	AL7									Yes No
	AM7									Ye:
			OTHER (In	cluding Aut	thor, Ti	tle, Date, Pertinent P	ages, e	tc.)		
	AN	7 MC881 sps.cor	10UMAD: N	1C88110 Sec 88110umad	cond Ge	eneration RISC Microp Motorola, Inc., 1 page (processo January	or User's Mai 24, 1997).	nual, at http://	www.mot-
	AO	7 Weiss, page, (R., <i>ARM Pi</i> November 1	ccolo Mixes . 996).	ARM R	ISC with DSP, at http:/	//ww.es	d.com/Editor	rial/1996/11/E	Briefs/arm.html, 1
	АР	<u>7</u>	<i>0C1x/C2x/C</i> (Bates Num	2xx/C5x Ass bers L07916	sembly 6-L0839	Language Tools User' 98).	s Guide	, Texas Instr	uments, 483	pages (March
	AQ	7 TMS32 (Bates	0C5x Gener Numbers L0	al-Purpose / 8399-L0856	Applicat 5).	iions User's Guide, Te	xas Inst	ruments, 167	' pages (July	1997).
•	AR	7 El-Shar	kawy, Moha e Hall PTR,	med, Ph.D., pp. 43-124 (<i>Digital</i> 1996).	Signal Processing App (Bates Numbers L065	olication 19-L066	s with Motor 601).	ola's DSP560	002 Processor,
EXAMINER	1							DATE CONS	IDERED	

ATTY. DOCKET NO. 1778.0100002 (0055.20US)

APPLICATION NO. 09/662,832

APPLICANT

INFORMATION DISCLOSURE STATEMENT

Van Hook et al.
FILING DATE

GROUP

							eptember 15, 2000		2183			
					U.S. I	PATEN	IT DOCUMENTS					
EXAMINER INITIAL		DOCUMENT NUMBER		DATE		NAME		CLASS	SUB- CLASS	FILING DATE		
	AA8											
	AB8					<u> </u>						
· · · · · · · · · · · · · · · · · · ·	AC8					↓						
-	AD8					-						
	AE8		·							 		
· · · · · · · · · · · · · · · · · · ·	AF8					 						
	AG8 AH8	+				 			_			
	Al8	┥			 	+				<u> </u>		
] Alo			<u> </u>	FOREIG	N PAT	ENT DOCUMENTS			<u></u>	<u></u>	
EXAMINER		DOCUMENT NUI				·	COUNTRY		CLASS	SUB- CLASS	TRANSLATION	
	AJ8										Yes No	
	AK8										Yes No	
	AL8			-							Yes No	
	AM8										Yes No	
	OTHER (Including Author, Title, Date, Pertinent Pages, etc.)											
	AN	8	Killian, E., "MIPS Extensions for Digital Media," <i>Microprocessor Forum</i> , pp. 1- 5 (October 22-23, 1996).									
	AO	8	"Interrupt Latency in the MC88110," <i>Motorola Semiconductor Engineering Bulletin</i> , Motorola, pp. 1, 2 and 4-9 (1992).									
	AP	<u>8</u>	"Running the MC88110 in Lockstep," <i>Motorola Semiconductor Engineering Bulletin</i> , Motorola, 2 pages (1992).									
	AQ	<u>8</u>	Turley, J., "Siemens TriCore Revives CISC Techniques: New 32-Bit Design Emphasizes DSP Capability and Microcontroller Functions," <i>Microprocessor Report</i> , Microdesign Resources, pp. 13-16 (November 17, 1997).									
, , , , , , , , , , , , , , , , , , ,	AR	<u>8</u>	i860™ Microprocessor Family Programmer's Reference Manual, Intel Corporation, 79 pages (1992). (Bates Numbers L09361-L09439).									
EXAMINER		L	1						DATE CONS	SIDERED		
									 			

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in

conformance and not considered. Include copy of this form with next communication to Applicant.